

a first transistor and a second transistor;

said first transistor having a first transistor gate, a first transistor source and a first transistor drain, where said first transistor gate is connected to a select line, said first transistor source is connected to a data line and said first transistor drain is connected to a second transistor gate of said second transistor;

said second transistor having said second transistor gate, a second transistor source and a second transistor drain, where said second transistor source is connected to said data line and second transistor drain is connected to an electroluminescent cell;

during [a] each of said LOAD [period] periods and when a select line signal on the select line activates the first transistor, said data line supplies, through said first transistor, a data signal to the second transistor gate where said data signal is stored; and

during [an] each of said ILLUMINATE [period] periods, said data line supplies a gray scale control signal to said second transistor, when said data signal stored at said second transistor gate exceeds the gray scale control signal on said data line, said second transistor applies energy from a power supply to said electroluminescent cell.

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20. (amended) An electroluminescent display comprising an array of pixels for providing gray scale illumination during a frame period, where said frame period is divided into a plurality of LOAD periods and a plurality of ILLUMINATE periods, where each LOAD period is followed by an ILLUMINATE period, each pixel comprising:

a control circuit, connected to a select line, a data line and a first electrode of an electroluminescent cell, for selectively applying energy to said electroluminescent cell in response to signals carried by said select line and said data line;